

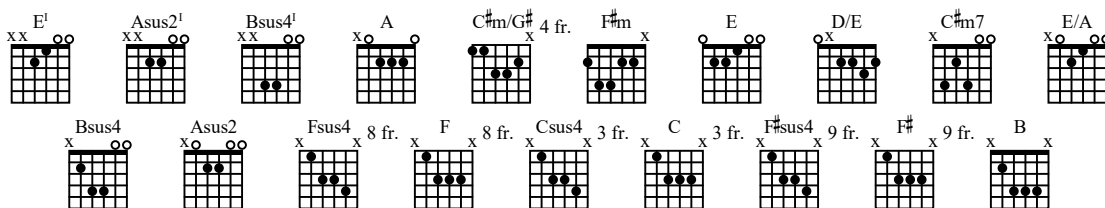
# BORN TO RUN

As recorded by Bruce Springsteen

(From the 1975 Album BORN TO RUN)

Transcribed by Doetje2

Words and Music by Bruce Springsteen



## A Intro

Moderately ♩ = 142

E

(Asus2)

Bsus4

1

Musical notation for the Intro section. It features a treble clef, a key signature of three sharps (F#, C#, G#), and a 4/4 time signature. The notation includes a series of chords: E, (Asus2), and Bsus4. Below the staff, there are guitar tablature lines for Treble (T), Alto (A), and Bass (B) staves. The T staff shows a sequence of notes: 0, 2, 1, 4. The A staff shows: 0, 2, 2, 4. The B staff shows: 0, 2, 2, 4. There are also fret numbers in parentheses: (0), (1), (2), (2), (0).

## B Verse

5

Musical notation for the Verse section. It features a treble clef, a key signature of three sharps (F#, C#, G#), and a 4/4 time signature. The notation includes a series of chords: E<sup>1</sup>, Gtr III, Gtr I, and Gtr II. Below the staff, there are guitar tablature lines for Treble (T), Alto (A), and Bass (B) staves. The T staff shows a sequence of notes: 0, 1, 2, 2, 0. The A staff shows: 0, 1, 2, 2, 0. The B staff shows: 0, 1, 2, 2, 0. There are also fret numbers in parentheses: (0), (0), (1), (2), (2), (0).

Asus2<sup>1</sup> Bsus4<sup>1</sup> E<sup>1</sup>

Gtr II  
P.M.-----

T  
A  
B 0 0 0 0 0 0 0 0 2 2 2 2 2 2 2 0 0 0 0 0 0 0 0

Asus2<sup>1</sup> Bsus4<sup>1</sup>

P.M.-----

T  
A  
B 0 0 0 0 0 0 0 0 0 0 0 0 0 0 2 2 2 2 2 2 2

**C** Pre-Chorus

Gtr II A C#m/G# F#m

Gtr IV

T 12 14 14 12 14 13 14 12 14 11 14 14 12 14

A 14 14 14 14 13 13 14 14 11 11 14 14

B

C#m/G# E D/E

T 12 14 12 14 9 12 10

A 13 13 9 10

B 9 9 12

A C#m/G# F#m

T 12 14 12 14 12 14 12 14

A 14 14 14 13 13 14 11 14 11 14 12 14

B

C#m/G# E C#m7

22

T 12 14 13 14 12 14

A 13 14 13 14 12 14

B 9 12 9 12 9 12

E/A Bsus4 To Coda

25

T 9 12 12 12

A 9 9 9 9

B 9 9 9 9

**D** Chorus

E (Asus2) Bsus4

27

Gtr I *f*

T 0 0 0 0

A 1 1 1 1

B 2 2 2 2

E (Asus2) 1. Bsus4 2. Bsus4

31

*f*

T 0 0 0 0

A 1 1 1 1

B 2 2 2 2

Gtr V

T 7 7

A 7 7

B 7 7

Gtr I E

36

Gtr V

T 8 9 12 12 12 12 12 14 12 12 12 12

A

B

sl. sl. sl. sl.

Asus2 Bsus4 E

38

T 8 9 12 12 12 12 12 14 12 12 14 12

A

B

sl. sl. sl. sl. sl. 1 1/2 Full Full 12 9 12 14 12 14 12

sl. sl. P P

Asus2 Bsus4

41

T 12 11 9 11 9 11 9 11

A 6 4 6 9 6 9 7 5 7 9 7 9 12 9 12 14 12 14 14 16

B

P P P sl. P H P H P H P H P H P H sl.

C#m9 B E Bm7

44

Gtr V

T 14 17 (17) (17) 17 (17)

A

B

sl. sl.

Gtr I

T 0 12 0 12 0 (0)

A 13 13 13 8 1 (0)

B 13 13 13 7 2 (0)

**E Bridge**

48

Dsus4 D Dsus4 D Gsus4 G

Gtr I

T 8 7 8 7 3 3

A 7 7 7 7 5 4

B 5 5 5 5 3 3

51

Gsus4 G Asus4 A Asus4 A

T 5 3 5 5 7 5

A 5 5 7 7 7 7

B 3 x 5 x 5 x 5

54

Csus4 C Csus4 C Dsus4 D

T 8 8 10 8 12 10

A 10 9 10 9 12 11

B 8 x 8 x 10 x 10

57

Gsus4 G Gsus4 G

T 12 10 10 11 3 3

A 12 12 12 14 5 4

B 10 x 10 x 3 x 3

60

Asus4 A Asus4 A Csus4 C Csus4 C

T 5 5 5 5 10 8

A 7 7 7 7 10 9

B x x x x x x

**[F] Interlude**

64

Fsus4  
Gtr IV

F

Fsus4

F

Gtr I

66

Fsus4

F

Csus4

C

68

Csus4

C

Fsus4

F

1/2

70

F#sus4

F#

F#sus4

F#

72

T

A

B

4 4 5 5 5 5 5 5 6 2

2 1 0 (0) 4 3 2

sl.

75

T  
A  
B 1 0 6 5 4 (4) 3 3 3 3 2 (2) (2) (2)

**G** Verse

81

E Asus2 Bsus4

Gtr II

Gtr IV

T 9 (9) 10 9  
A 9 9 9 9  
B 7 (7) 9 11 9 11

85

E Asus2 Bsus4

Gtr II

Gtr IV

T 9 (9) 10 9  
A 9 9 9 9  
B 7 (7) 9 11 9 11

*D.S. al Coda*

89

E C#m7 E/A Bsus4

Gtr II

Gtr IV

T 12 12 9 12 9 9 12 12  
A 9 9 9 9 9 9 9 9  
B 9 9 9 9 9 9 9 9

93

E C#m7 E/A Bsus4

T 12 12 9 12 9 9 12 12  
A 9 9 9 9 9 9 9 9  
B 9 9 9 9 9 9 9 9

**H** Outro

97

Gtr II E Asus2 Bsus4

Gtr I

101

E Asus2 Bsus4

105

E Asus2 Bsus4

109

E Asus2 *rit.* A B E

T A B

T A B

T A B

T A B